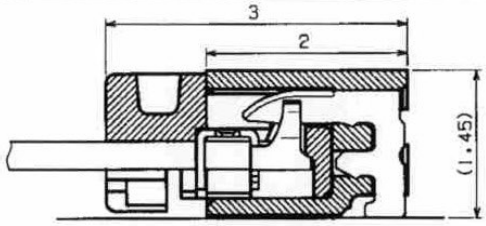
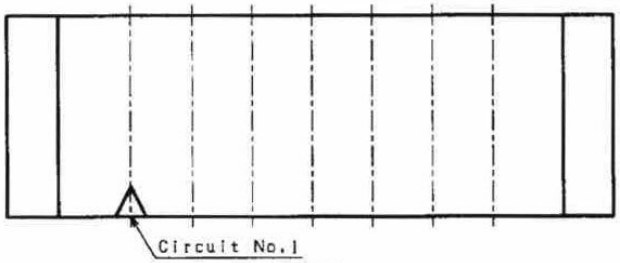
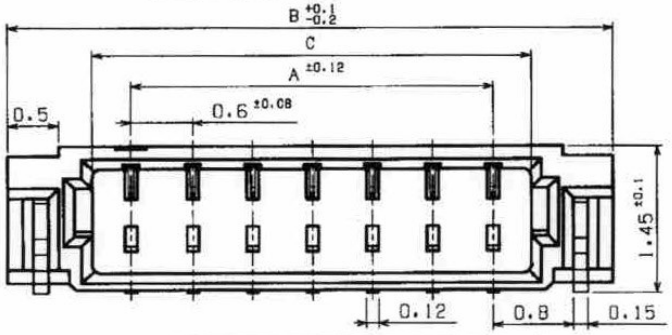
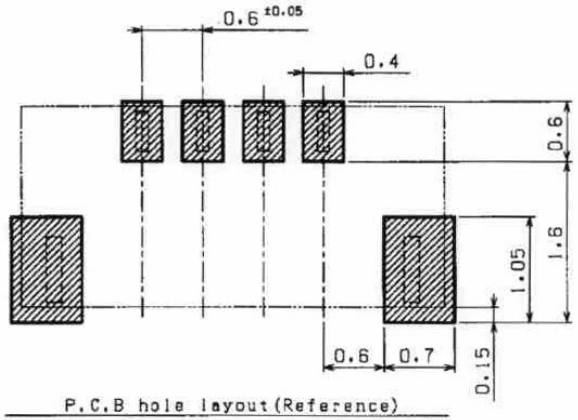
REV.	DESCRIPTION	DATE	DESIGNED
1	Circuit No. is added.	AUG.23.2006	M.H



Assembly layout



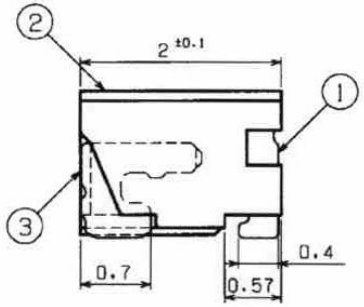


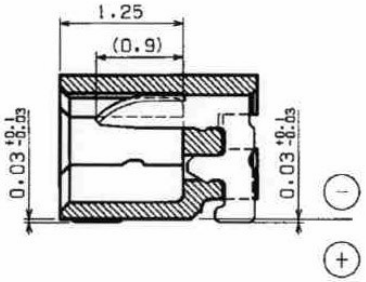


P.C.B hole layout(Reference)
(Viewing from connector mounted side)

NOTE

1. Unless otherwise specified, tolerance are ±0.3
2. Coplanarity of soldertail and solder tab shall be 0.1max
3. Tolerance of pattern pitch on PCB are not cumulative.





JST CORP
JAN 30 2007
WEC

Circuits	Dimensions A	Dimensions B	Dimensions C
2	0.6	3.0	1.36
4	1.8	4.2	2.56
6	3.0	5.4	3.76
10	5.4	7.8	6.16

Circuits	Dimensions A	Dimensions B	Dimensions C
16	9.0	11.4	9.76
20	11.4	13.8	12.16
22	12.6	15.0	13.36

No.	PART NAME	MATERIAL	SURFACE FINISH	REMARKS
3	SOLDER TAB	BRASS	COPPER-UNDERPLATED TIN-PLATED	t0.15
2	WAFER	POLYAMIDE		UL94V-0, NATURAL
1	BASE CONTACT	COPPER ALLOY	COPPER-UNDERPLATED TIN-PLATED	t0.12

SIZE	UNIT	SCALE	PROJECTION	DATE	CUSTOMER
A3	METRIC	20:1		JUL.25.2005	

APPROVED	CHECKED	DESIGNED	DRAWN
S.S	T.T	M.H	M.O

JST J.S.T.MFG.CO., LTD.

SERIES NAME	XSR CONNECTOR	
PART No.	SM( )B-XSRS (LF)(SN)	
DRAWING No.	KRD-37169-1	R1

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EC4109-2